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JD Collins, H Wang, DM Tullsen, C Hughes, ... - ... Proceedings, 28th Annual International Symposium on, 2001 ieeexplore lece.org

... If only the non-speculative thread spawns speculative threads, performance

gains of up to 30% are achieved when assuming ideal hardware. ...

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## Clustered speculative multithreaded processors- > psu.edu repri

P Marcuello, A González - Proceedings of the 13th international conference on .... 1999 - portal acm.org

... non-speculative thread reaches the point where the first speculative thread started,

it terminates and the first speculative thread becomes non-speculative ...

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#### Dynamic speculative precomputation- > ucsd.edu ppp

JD Collins, DM Tullsen, H Wang, JP Shen - Proceedings of the 34th annual ACM/IEEE ..., 2001 - portal acm.org ... This structure can be implemented in hardware [16, 12], eliminating fetch con-flicts

between speculative and non-speculative threads, or in software as a ...

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#### Speculative versioning cache- \* kfupm.edu.sa repri

S Gopal, TN Vijaykumar, JE Smith, GS Sohi - Proceedings of the 4th International Symposium on ..., 1998 dol.ieeecomputersociety.org

#### ... 2.1.3. Task commits and squashes The oldest active task is non-speculative and can

commit its speculative mem- ory state (versions created by stores from this ...

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### IPDFI A delay model and speculative architecture for pipelined routers

LS Peh, WJ Dally - ... Symposium on High-Performance Computer Architecture, 2001 - princeton.edu

... A way to achieve this is shown in Figure 7(c), where two switch allocators handle

non-speculative and speculative requests in parallel, and successful non ...

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# Integrated predicated and speculative execution in the IMPACT EPIC architecture-

▶ cmu.edu (ron)

DI August, DA Connors, SA Mahlke, JW Sias, ... - ACM SIGARCH Computer Architecture News, 1998 doi.leeecomputersociety.org

... First, it is important to distinguish speculative operations from non-speculative operations, since operations which have not been control speculated should ...

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#### Hardware and software support for speculative execution of sequential binaries on a chip- ...kfupm.edu.sa mon

V Krishnan, J Torrellas - Proceedings of the 12th international conference on ..., 1998 - portal.acm.org

... This hardware should separate speculative updates from non-speculative ones and

must allow speculative threads to acquire data from the appropriate producer ...

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# Architectural support for scalable speculative parallelization in shared-memory ...-

- ▶ kfupm.edu.sa (PDF)
- M Cintra, JF Martinez, J Torrellas ACM SIGARCH Computer Architecture News, 2000 portal.acm.org ... In general, a speculative thread will become non-speculative only when all its
- predecessors have committed, which guarantees that all its memory operations are ...

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- J González, A González Proceedings of the 11th international conference on ..., 1997 portal acm.org
- ... Speculative execution has been the focus of several recent proposals that ... proposals the memory access is overlapped with the non-speculative effective address ...

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IPSI Single-program speculative multithreading (SPSM) architecture: Compiler-assisted fine-

- DK Dubey, KO'Brien, KM O'Brien, C Barton Proc. Int. Conf. on Parallel Architectures and ..., 1995 domino.watson.ibm.com
- ... As explained above, machines with single control ow have to rely on the compiler

to group together speculative and/or non-speculative data-independent ... Cited by 110 - Related articles - View as HTML - All 2 versions

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